

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning at page 7, line 13, with the following amended paragraph:

Signals generated by the waveform generator 22 are primarily specified to cover requirements of control 16M, 64M and 256M SDRAMs and SGRAMs (Synchronous Graphics RAM) in configurations of up to 16 banks and up to 36 bits width. Another bunch of signals is provided to drive other units in the tester. Some spare signals are reserved for further extensions. Waveforms are stored in the waveform RAM [[53]] 43 as 32-bit words, one word per clock period at the DUT.